## THE CATHOLIC UNIVERSITY OF EASTERN AFRICA

## A. M. E. C. E. A

GABA CAMPUS - ELDORET
MAIN EXAMINATION

## SEPTEMBER - DECEMBER 2021 TRIMESTER

## FACULTY OF SCIENCE

BACHELOR OF SCIENCE
DEPARTMENT OF COMPUTER AND INFORMATION SCIENCE CMT 201: LOGIC CIRCUITS

## Date: December 2021 <br> Duration: 2 Hours <br> Instructions: Answer Question ONE and any other THREE Questions

## QUESTION ONE

i) Write the word TEACHER in binary.
ii) Explain the principal of duality in Boolean algebra. How is it useful?
(4 Marks)
iii) Prove following rules by the method of perfect induction.
a) $A \cdot B+A \cdot B=A$
b) $(A+B) \cdot \overline{(A \cdot B})=A \cdot \bar{B}+B \cdot \bar{A}$
iv) State and prove the two basic De Morgan's theorems.
(5 Marks)
v) Express the Boolean function $f=A+\bar{B} . C$ in sum-of-minterms (products) form.
(4 Marks)
vi) Describe the purpose and operation of each of the following:
a) AND gate.
(3 Marks)
b) OR gate.
(3 Marks)
c) NOT gate.

## QUESTION TWO

i) Why was BCD code extended to EBCDIC?
ii) State THREE types of combinational logic elements and THEE types of sequential logic elements.
iii) What is an exclusive OR gate (also called an EOR, or XOR gate)? Provide a truth table for an exclusive OR gate and explain in plain English what it does.
(10 Marks)

## QUESTION THREE

a) For the circuit below, draw a truth table and provide the values of the output at points $P, Q, R, S$, and $T$.

i) Write the full form of the following abbreviations:
a) $B C D$
b) ASCII
c) EBCDIC
d) UTF
ii) Differentiate between a bit and a byte?
(2 Marks)

## QUESTION FOUR

i) Carefully discuss the sum-of-products and product-of-sums with an example
ii) Discuss the advantages and disadvantages of performing various arithmetic operations by additive method in a digital computer.
iii) Subtract 5610 from 9210 using complementary method.

## QUESTION FIVE

i) Convert the decimal number 123 into the following bases:
a) binary
b) hexadecimal
ii) Why is two's complement arithmetic so widely used by digital computers?
iii) What is the answer to the following binary addition operation? Assume that the numbers are in 8-bit signed two's complement format.

$$
00011101
$$

+11110011
iv) Convert the two binary values in part c) and your result into decimal form and comment on the result.
v) State and prove Involution Law.

## *END*

